VIGNAN'S
Foundation for Science, Technology & Research
(Deemed to be University)
-Estd. u/s 3 of UGC Act 1956

Department of Electronics and Communication engineering

MTech -VLSI Design

Action Taken Report on the feedback obtained in AY2019-20

31/07/2020

Stakeholder	Comments/Suggestions	Action Taken or Suggested
Alumni	Need some more emphasis on Communication and Soft Skills to improve the placements	Employability Orientation is offered as a course with two credits in II Semester.
	Need to increase the complexity and toughness in Digital design lab programs. Need to add more FSM related designs with real time example.	More emphasis given on simulation/software-based experiments for real time examples.
Student	Programming languages/Scripting Languages should be strengthened.	Strengthened the Programming languages/Scripting Languages by adding practical.
	They suggested to increase core industry internships	Provision is given for one year industry internships
Employers	Instead of only Python, they suggested to include TCL/TK in scripting languages.	Strengthened scripting languages course by adding Python and TCL/TK.
Faculty	Suggested to have system level projects which supports society.	Mini projects and Societal Centric/ Industry standard Project is added.
	Suggested to include course on imparting research culture among the students.	To impart research orientation among students, Research Methods is offered as a three-credit course.
Parent	Include CRT for MTech students also.	It is already available and interested students can attend.
	Courses on emerging skills are highly needed	As per the suggestion, we have included very good number of courses on emerging fields of VLSI Testing and Verification.

HOD-ECE